

HAT2183WP

Silicon N Channel Power MOS FET Power Switching

REJ03G0530-0500

Rev.5.00

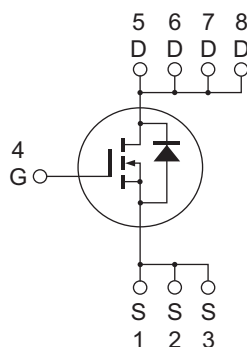
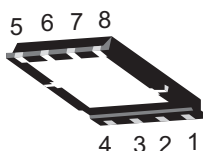
Oct 21, 2005

Features

- Low on-resistance
- Low drive current
- High density mounting

Outline

RENESAS Package code: PWSN0008DA-A
(Package name: WPAK)



1, 2, 3 Source
4 Gate
5, 6, 7, 8 Drain

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to Source voltage	V_{DSS}	150	V
Gate to Source voltage	V_{GSS}	±30	V
Drain current	I_D	20	A
Drain peak current	$I_{D(pulse)}$ ^{Note1}	40	A
Body-Drain diode reverse Drain current	I_{DR}	20	A
Body-Drain diode reverse Drain peak current	$I_{DR(pulse)}$ ^{Note1}	40	A
Avalanche current	I_{AP} ^{Note3}	20	A
Avalanche energy	E_{AR} ^{Note3}	30	mJ
Channel dissipation	P_{ch} ^{Note2}	30	W
Channel to case thermal impedance	θ_{ch-c}	4.17	°C/W
Channel temperature	T_{ch}	150	°C
Storage temperature	T_{stg}	-55 to +150	°C

Notes: 1. $PW \leq 10 \mu s$, duty cycle $\leq 1\%$

2. Value at $T_c = 25^\circ C$

3. $STch = 25^\circ C$, $T_{ch} \leq 150^\circ C$

Electrical Characteristics

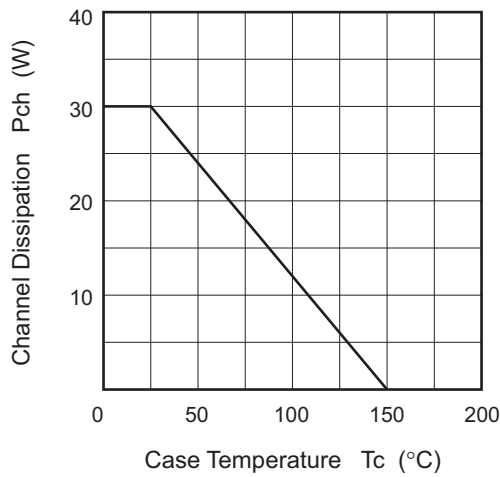
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to Source breakdown voltage	$V_{(BR)DSS}$	150	—	—	V	$I_D = 10 \text{ mA}$, $V_{GS} = 0$
Zero Gate voltage Drain current	I_{DSS}	—	—	1	μA	$V_{DS} = 150 \text{ V}$, $V_{GS} = 0$
Gate to Source leak current	I_{GSS}	—	—	± 0.1	μA	$V_{GS} = \pm 30 \text{ V}$, $V_{DS} = 0$
Gate to Source cutoff voltage	$V_{GS(off)}$	3.0	—	4.5	V	$V_{DS} = 10 \text{ V}$, $I_D = 1 \text{ mA}$
Forward transfer admittance	$ y_{fs} $	9	15	—	S	$I_D = 10 \text{ A}$, $V_{DS} = 10 \text{ V}$ ^{Note4}
Static Drain to Source on state resistance	$R_{DS(on)}$	—	0.057	0.064	Ω	$I_D = 10 \text{ A}$, $V_{GS} = 10 \text{ V}$ ^{Note4}
Input capacitance	C_{iss}	—	1200	—	pF	$V_{DS} = 25 \text{ V}$
Output capacitance	C_{oss}	—	260	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	C_{rss}	—	25	—	pF	$f = 1 \text{ MHz}$
Turn-on delay time	$t_{d(on)}$	—	32	—	ns	$I_D = 10 \text{ A}$
Rise time	t_r	—	53	—	ns	$V_{GS} = 10 \text{ V}$
Turn-off delay time	$t_{d(off)}$	—	69	—	ns	$R_L = 7.5 \Omega$
Fall time	t_f	—	11	—	ns	$R_g = 10 \Omega$
Total Gate charge	Q_g	—	27	—	nC	$V_{DD} = 120 \text{ V}$
Gate to Source charge	Q_{gs}	—	7	—	nC	$V_{GS} = 10 \text{ V}$
Gate to Drain charge	Q_{gd}	—	10	—	nC	$I_D = 20 \text{ A}$
Body-Drain diode forward voltage	V_{DF}	—	0.88	1.4	V	$I_F = 20 \text{ A}$, $V_{GS} = 0$ ^{Note4}
Body-Drain diode reverse recovery time	t_{rr}	—	110	—	ns	$I_F = 20 \text{ A}$, $V_{GS} = 0$ $diF/dt = 100 \text{ A}/\mu\text{s}$

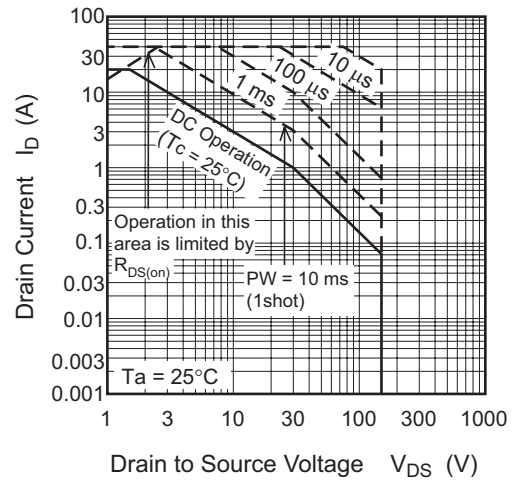
Notes: 4. Pulse test

Main Characteristics

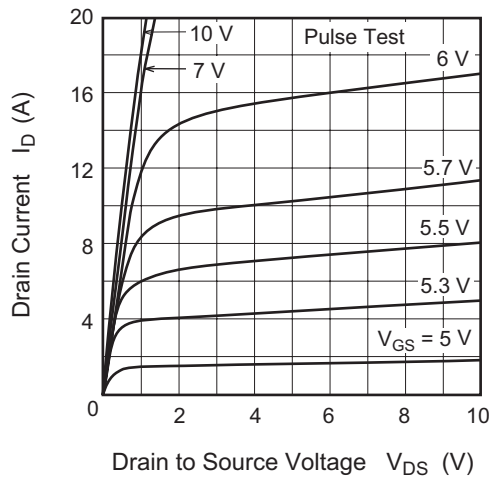
Power vs. Temperature Derating



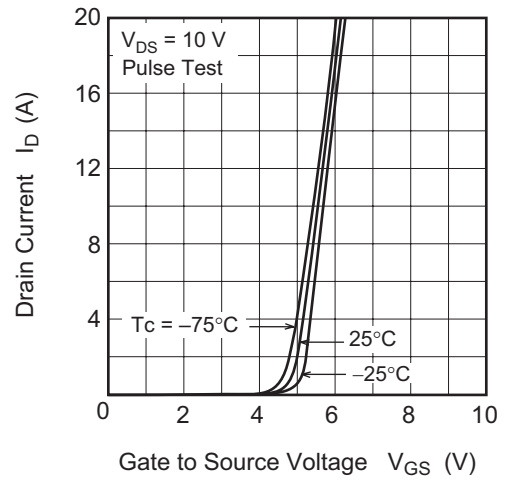
Maximum Safe Operation Area



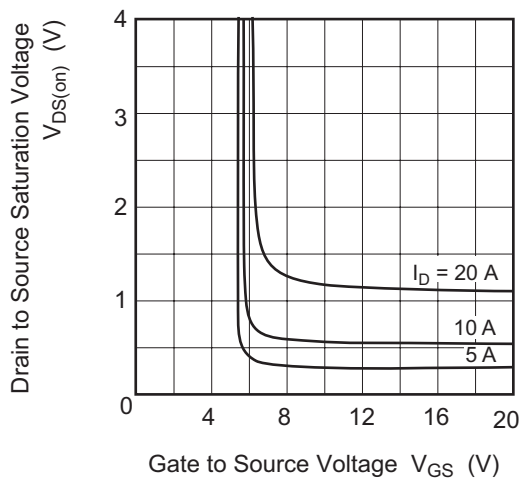
Typical Output Characteristics



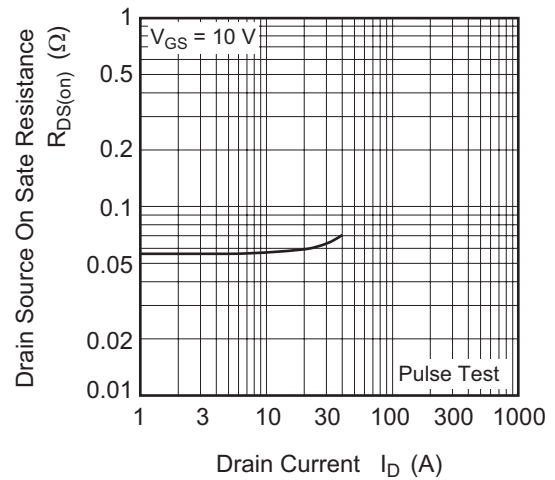
Typical Transfer Characteristics

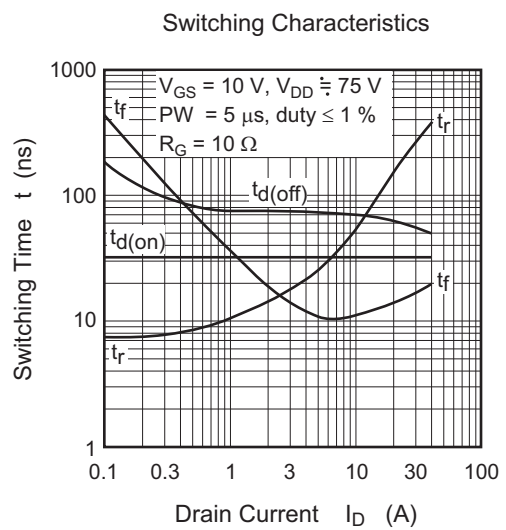
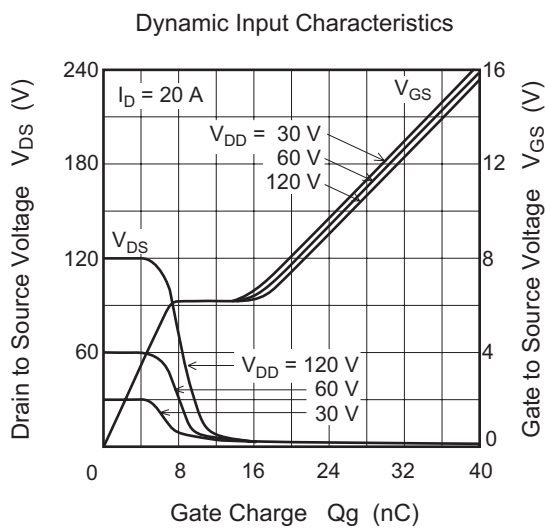
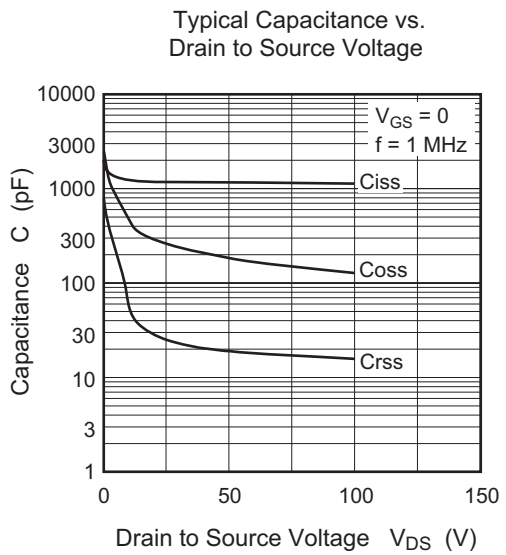
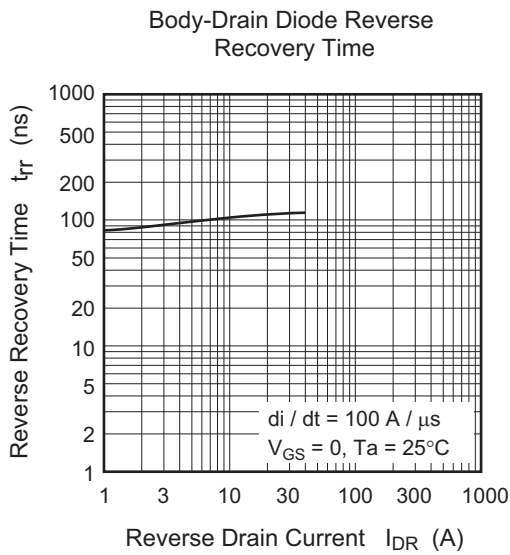
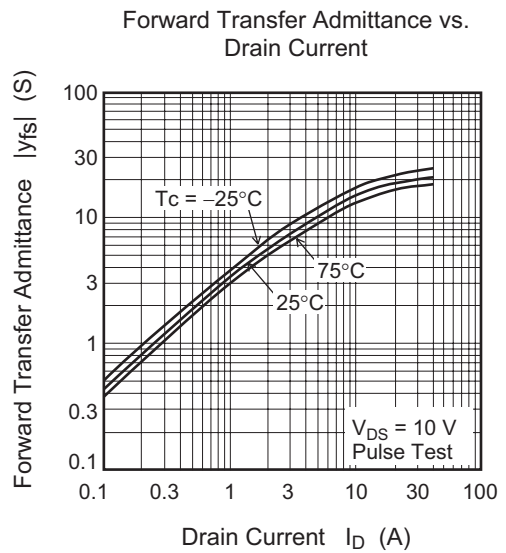
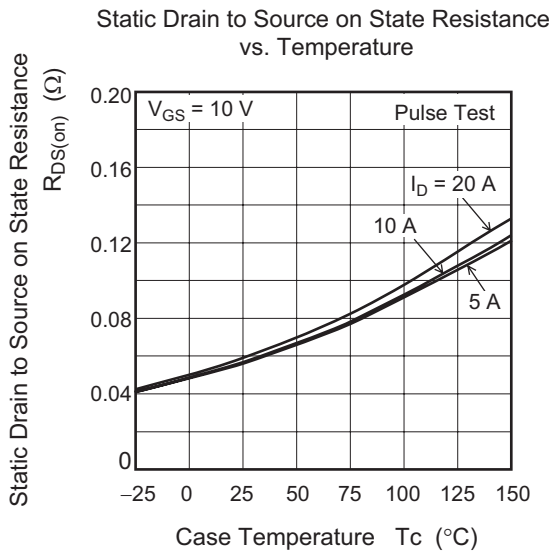


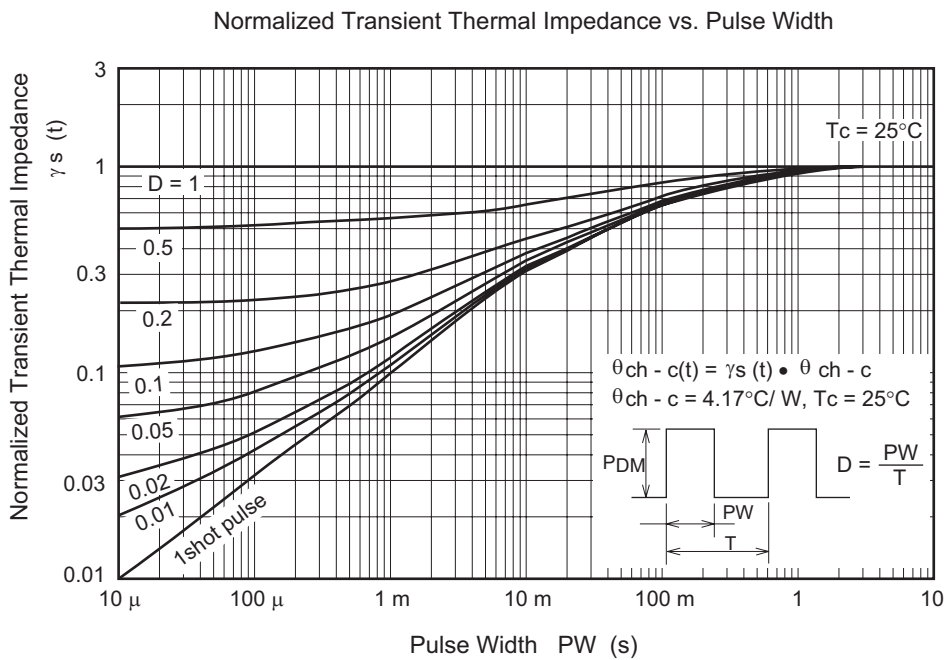
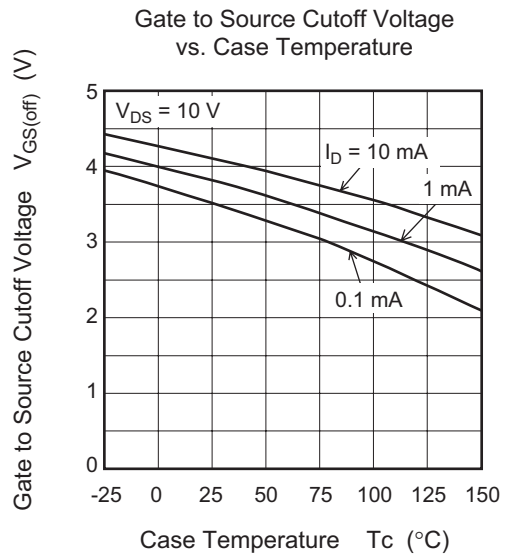
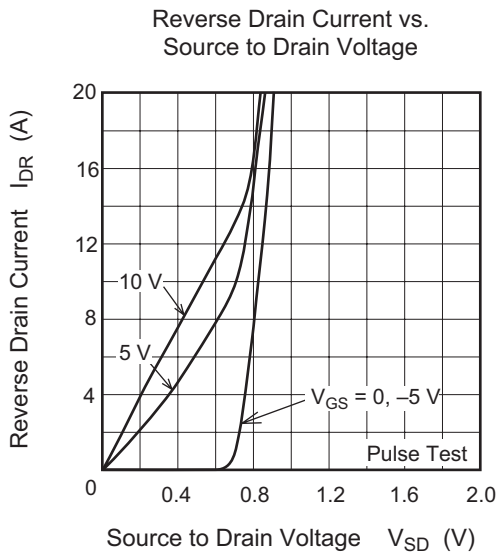
Drain to Saturation Voltage vs. Gate to Source Voltage



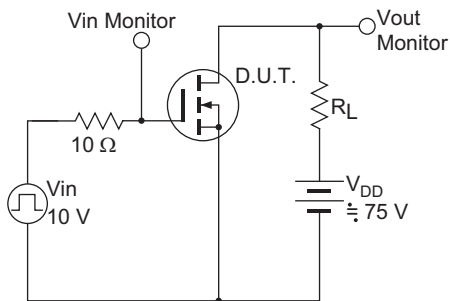
Static Drain to Source State Resistance vs. Drain Current



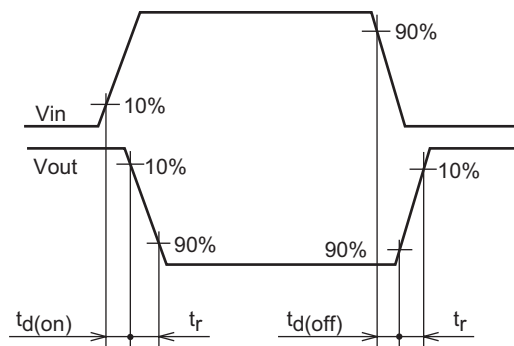




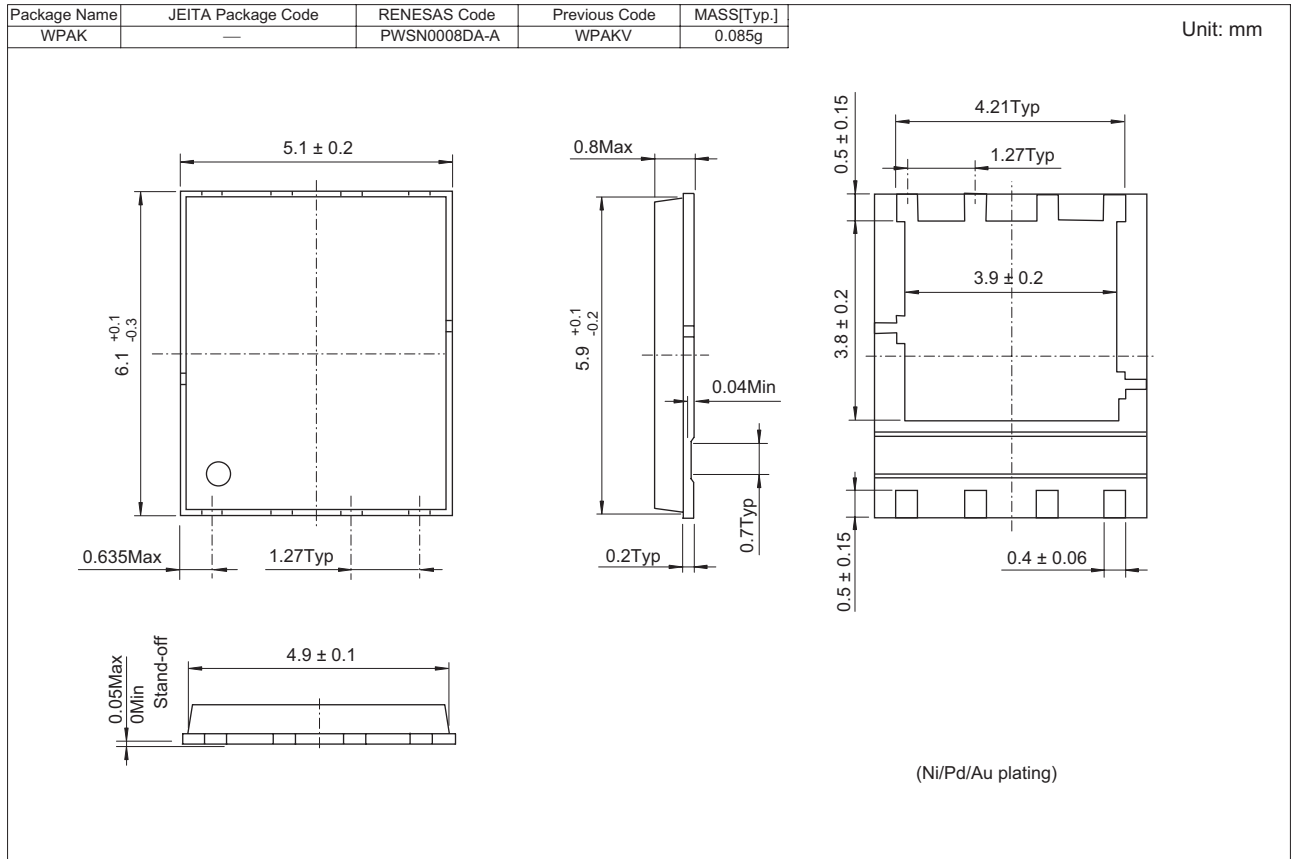
Switching Time Test Circuit



Waveform



Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
HAT2183WP-EL-E	2500 pcs	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.

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